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| <input type="checkbox"/> | L24 | L23 and (execut\$6 with status) | 13 |
| <input type="checkbox"/> | L23 | L22 and l21 | 131 |
| <input type="checkbox"/> | L22 | (717/136 717/137 717/138 717/139 717/140 717/141 717/142 717/143 717/144 717/145 717/146 717/147 717/148 717/149 717/150 717/151 717/152 717/153 717/154 717/155 717/156 717/157 717/158 717/159 717/160 717/161).ccls. | 858 |
| <input type="checkbox"/> | L21 | (translat\$6 or convert\$6) with (command\$2 or instruction\$2) with (platform\$2 or system\$2) | 3268 |
| <input type="checkbox"/> | L20 | (translat\$6 or convert\$6) with (command\$2 or instruction\$2) with (differnt) with (platform\$2 or system\$2) | 0 |
| | | <i>DB=USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L19 | L18 and @pd > 20040602 | 1 |
| <input type="checkbox"/> | L18 | L12 and (dispatch\$6 with (platform\$2 or system\$2)) | 10 |
| <input type="checkbox"/> | L17 | L12 and (dispatch\$6 with (different or multiple) near3 (platform\$2 or system\$2)) | 0 |
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| <input type="checkbox"/> | L14 | L13 and (execut\$6 with status) | 11 |
| <input type="checkbox"/> | L13 | L12 and (execut\$6 with (concurrent\$3 or parallel\$6)) | 29 |
| <input type="checkbox"/> | L12 | L10 and L11 | 114 |
| <input type="checkbox"/> | L11 | (717/136 717/137 717/138 717/139 717/140 717/141 717/142 717/143 717/144 717/145 717/146 717/147 717/148 717/149 717/150 717/151 717/152 717/153 717/154 717/155 717/156 717/157 717/158 717/159 717/160 717/161).ccls. | 1845 |
| <input type="checkbox"/> | L10 | L8 and ((different or multiple) near3 (platform\$2 or system\$2)) | 158 |
| <input type="checkbox"/> | L9 | L8 and ((different or multiple) with (platform\$2 or system\$2)) | 206 |
| <input type="checkbox"/> | L8 | L2 and L5 | 305 |
| <input type="checkbox"/> | L7 | (translat\$6 or convert\$6) with ((common command\$2) or (common instruction\$2)) with (platform\$2 or system\$2) | 14 |
| <input type="checkbox"/> | L6 | L4 and L5 | 18 |
| <input type="checkbox"/> | L5 | 717/\$.ccls. | 4653 |
| <input type="checkbox"/> | L4 | L3 and (execut\$6 with status) | 194 |
| <input type="checkbox"/> | L3 | L2 and (execut\$6 with (concurrent\$3 or parallel\$6)) | 730 |

- ☐ L2 (translat\$6 or convert\$6) with (command\$2 or instruction\$2) with (platform\$2 or system\$2) 11037
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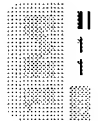
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Pages:30 - 57[\[Abstract\]](#) [\[PDF Full-Text \(561 KB\)\]](#) **IEEE JNL****2 Distributed audit trail analysis***Mounji, A.; Le Charlier, B.; Zampunieris, D.; Habra, N.;*Network and Distributed System Security, 1995., Proceedings of the Symposium on , 16-17 Feb. 1995
Pages:102 - 112[\[Abstract\]](#) [\[PDF Full-Text \(844 KB\)\]](#) **IEEE CNF****3 A next generation expert system for flexible assembly***Rubin, S.H.;*Systems Engineering, 1992., IEEE International Conference on , 17-19 Sept. 1
Pages:393 - 398[\[Abstract\]](#) [\[PDF Full-Text \(376 KB\)\]](#) **IEEE CNF****4 Merging voice communications and information processing: using a platform for voice information services***McAllister, M.J.; Jackson, A.L.;*Computers and Communications, 1990. Conference Proceedings., Ninth Annual International Phoenix Conference on , 21-23 March 1990
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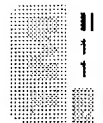
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Reverse Engineering, 2002. Proceedings. Ninth Working Conference on , 29 Oc Nov. 2002

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[\[Abstract\]](#)[\[PDF Full-Text \(580 KB\)\]](#)**IEEE CNF****2 A language for software subsystem composition***Buffenberger, J.; Gruell, K.;*

System Sciences, 2001. Proceedings of the 34th Annual Hawaii International Conference on , 3-6 Jan. 2001

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[\[Abstract\]](#)[\[PDF Full-Text \(320 KB\)\]](#)**IEEE CNF****3 Towards automatic synthesis of QoS preserving implementations for object-oriented design models***Saksena, M.;*

Object-Oriented Real-Time Dependable Systems, 1999. Proceedings. Fifth International Workshop on , 18-20 Nov. 1999

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Parallel Processing Symposium, 1994. Proceedings., Eighth International , 26-

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5 Predicting the performance of scientific applications on distributed memory multiprocessors

Abramson, D.;

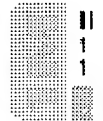
Scalable High-Performance Computing Conference, 1994. Proceedings of the ,
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Pages:285 - 292

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Software Engineering, IEEE Transactions on , Volume: 24 , Issue: 2 , Feb. 1999

Pages:125 - 136

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) **IEEE JNL****2 Learning about digital signal processing using spreadsheets and simulation software***Baicher, G.S.; Sherrington, J.A.;*

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Pages:41 - 48

[\[Abstract\]](#) [\[PDF Full-Text \(748 KB\)\]](#) **IEEE JNL****3 Versatile real time microprocessor development system***Abdelbaki, H.E.M.;*

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Pages:C32 - 1-8

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Giorgi, R.; Prete, C.A.; Prina, G.;

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